

LIST OF CONTENTS

NUMBER 1/2

P. D. Stigall and Saeed Azimi

P. D. Stigall and Saeed Azimi

Udai Garg and Yo-Ping Huang

Michael Andrews and Foo Lam

K. Y. Srinivasan and A. K. Sood

Rajiv Sharma and T. N. Rajashekara

i Softstrip® computer-readable data strip containing the table of contents of this issue of *Computers & Electrical Engineering*

1 Optimization of a topologically tree-based local area network

11 Queuing analyses of a topologically tree-based local area network

29 Improving the performance of banyan networks

35 Architectural directions for microsequencers

43 Design of highly reliable tree architectures

53 Fault analysis and a unified design of PLAs for easy testability

I Software Survey Section

NUMBER 3/4

Bidyut Gupta, Bhargab B. Bhattacharya and Gopal C. Basu

Sajal K. Das, Vinod K. Agrawal, Dilip Sarkar and Lalit M. Patnaik

Mohamed Mostafa Saied and Abdullah S. Al-Fuhaid

A. K. Sood, S. Akhtar and K. Y. Srinivasan

Balbir S. Dhillon and Subramanyam N. Rayapati

A. B. Ektare and Maki K. H. Al-Sheakly

i Softstrip® computer-readable data strip containing the table of contents of this issue of *Computers & Electrical Engineering*

65 Testable design of two-dimensional cellular logic arrays for detecting stuck-at and bridging faults

75 Invariant-preserving Petri net reduction and conditions for invariant-existence

93 A computer relaying algorithm for direct determination of the fault distance

105 An extended token bus protocol for embedded networks

125 Reliability modelling of redundant computer systems with common-cause failures

137 Function symmetries and decoded-PLA realization

I Software Survey Section